

## RISC-V hypervisor implementation

*Monday, 9 September 2019 12:15 (45 minutes)*

The RISC-V hypervisor extension is carefully designed to be compliant with both Type-1 and Type-2 hypervisors. We have ported Xvisor (Type-1) and KVM (Type-2) for RISC-V architecture. In this session, we share our experience porting these hypervisors and also discuss future work on RISC-V hypervisors.

### **I agree to abide by the anti-harassment policy**

Yes

### **I confirm that I am already registered for LPC 2019**

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**Session Classification:** RISC-V MC